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(See p2 for detail)

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Memories

A/V switching

Channel decoding

Smart Card Interface on daughter board
 ACC COMPATIBLE
TDA8024

Front Panel module

Hard Modem 73M2901CL RJ11
 Sky back-end board

A/V switching

- TO TV-LINK MODULATOR
- CVBS cinch
- AUDIO CINCHES
- SVHS

STi5516
 388 BGA package

PIOs and Controls

- UART ASC0, UART ASC1 SmartCard0, UART SSC0
- UART SSC1, Extra PIOs
- PWM Modules, 27MHz z control - Oscillator
- Reset, Extra PIOs, Test controller, INT controller, MAFE or UART ASC4
- Real Time Clock, DCU link, Real Time Clock
- UART ASC2, IR Tx/Rx

Display Subsystem

- Background, Still plan, Video plan, HV resize, OSD plan, Sub pict plan
- 4:2:2, 4:2:2, 4:2:2, 4:4:4, 4:4:4, 4:4:4, 4:4:4, 4:4:4
- UV, UV, UV, UV, UV
- GenLock system, VBI insert : WSS, Txt, Closed Caption, HV sync, PAL, NTSC, SECAM, Macro. 7.01, YUV to RGB, RGB, YC, CVBS, Digital out

EMI Controller

- Ext CONTROL, Generic CONTROL, SDRAM CONTROL, DATA, ADDRESS

Local SDRAM Controller

- CONTROL, DATA, ADDRESS

MPEG2 MP@ML Video Decoder

ATA-5

MPEG1 LI&LII MPEG2 MC Dolby AC3

Audio DACs

TS0

- TS stream in, PTI stream in, PTI stream out, AV stream i1284 stream, Control, Link Layer Interface, TS Mux, PTI Demux-Descr DES-DVB-ICAM, IEEE1284 module, ST20 C2+ 120/166 MHz

FLASH MLC

FLASH MLC 4 MB
 TBGA64 package (1mm)

DATA (0:8), ADDRESS, CONTROL

D (0:15), A (1:24)

SDRAM CTRL / MPEG (SMI)

SDRAM 16 MByte
 TSOP54 package

DATA (0:15), ADDRESS, CONTROL

SDB(0:15), SDA(0:13)

EEPROM

Eeprom
 32 kByte
 SO8 package

CONTROL

I2C

Tuner on Board

MAX2116
 QFN 40 package

Channel decoding

LNB control LNBP15SP

Control, PIOs, QPSK, FEC

STV0299
 TQFP 64 package

TS stream

EMI bus

SMI bus

OUT_L/R

A/V switching

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AUDIO CINCHES

SVHS

Serial link RJ9