

NSP2340A-TB1

User Guide

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1. Overview

NSP2340A-TB1 is a tiny demo board for NSP2080A/2170A/2340A series.

2. PCB Image and Placement

The Figure 1 and Figure 2 show the PCB images of NSP2340A-TB1.

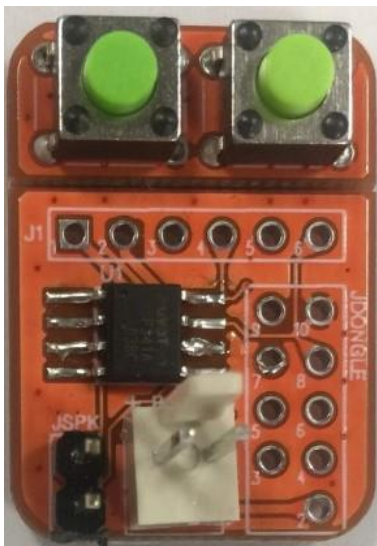


Figure 1. The Top Side



Figure 2. The Bottom Side

The Figure 3 and Figure 4 show the PCB placement of NSP2340A-TB1.

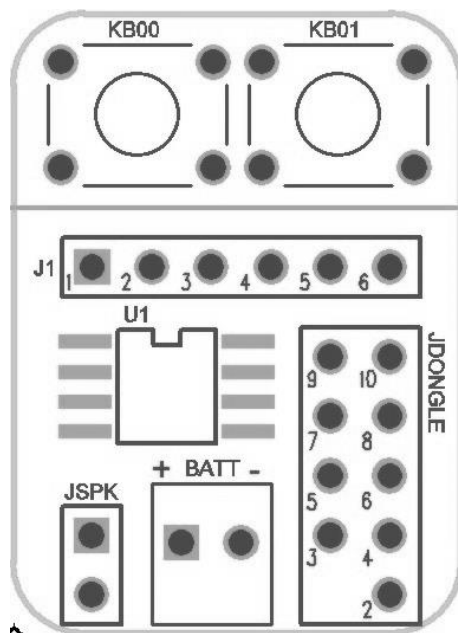


Figure 3. The Top Placement

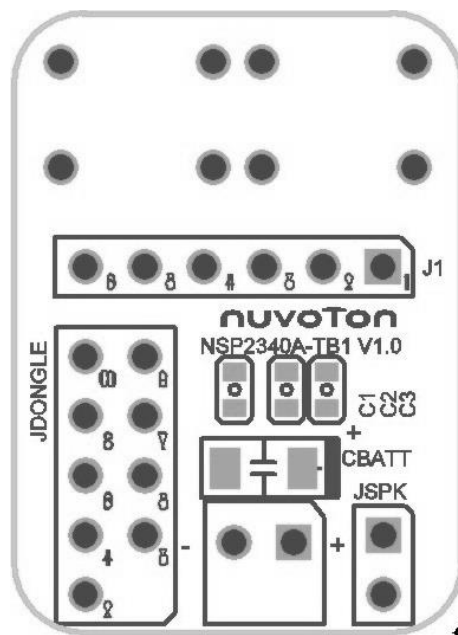


Figure 4. The Bottom Placement

3. Description

- BATT : Power supply input pin. The operating voltage is 2.0V ~ 5.5V.
- JSPK : Speaker output.
- C1 : Reset de-bounce capacitor. (0.1 μ F, it can be omitted.)
- C2, C3 : 0.1 μ F
- CBATT : 4.7 μ F
- KB00, KB01 : KB00 connect to BP00, KB01 connect to BP01.
- U1 : NSP2340A
- JDONGLE : Download the .obj file by NSP Writer.

	3 NC	5 NC		7 NC	9 NC
2 VDD	4 BP01	6 BP00		8 /RESET	10 VSS

- J1 :

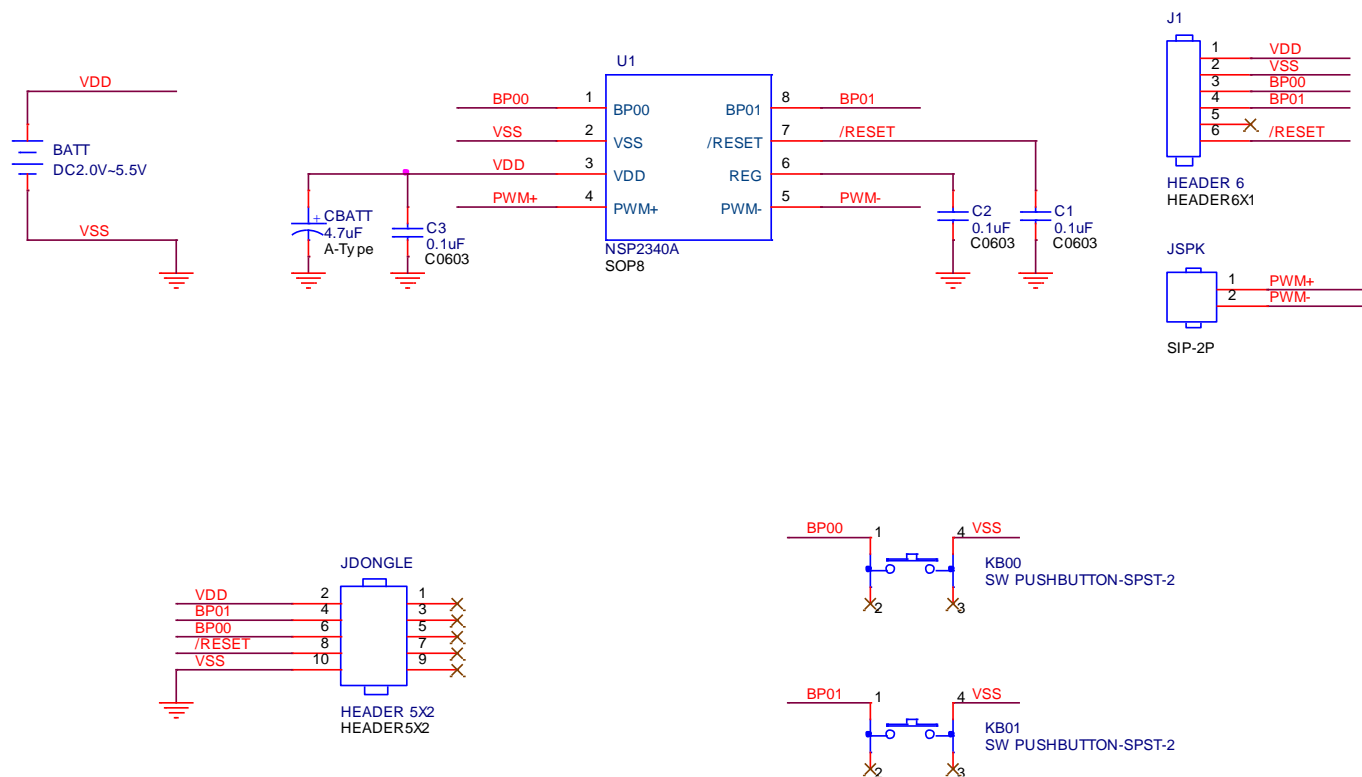
1 VDD	2 VSS	3 BP00	4 BP01	5 NC	6 /RESET
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4. NSP Writer Connect to NSP2340A-TB1

NSP2340A-TB1 can be controlled through NSP Writer. The connection between the NSP Writer and the board is shown as below.



5. Schematic



6. Revision History

Version	Date	Substantial Changes	Page
1.0	April 2022	Initial Release	All

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