

Version: C Issued Date: 2019/12/19

# **Approval Sheet**

# (產品承認書)

產品名稱 (Product): Bluetooth Low Energy Module

解決方案 (Solution): Nordic nRF52832 QFN Package

產品型號 (Model No.): MDBT42Q - U512KV2 (u.FL Connector)

產品料號 (Part No.): see 4.3 Order Code

#### Advantage of MDBT42Q-U series:

- 1. Long working distance: over 80 meters in open space.
- 2. Declaration ID includes all Nordic applied profiles.
- Three selected antennas to use directly with certification approval.
   See 7 Antenna.

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### 1. Overall Introduction

Raytac's MDBT42Q-U512KV2 is a BT 4.2, 5.0 & 5.1 stack (Bluetooth low energy or BLE) module designed based on **Nordic nRF52832 SoC solution**, which incorporates: **GPIO**, **SPI**, **UART**, **I2C**, **I2S**, **PWM**, **ADC** and **NFC** interfaces for connecting peripherals and sensors.

#### Features of the module:

- 1. Dual Transmission mode of BLE & 2.4Ghz RF upon customer's preference.
- 2. Compact size with (L) 16 x (W) 10 x (H) 2.2 mm.
- 3. Low power requirements, ultra-low peak, average and idle mode power consumption.
- 4. Be compatible with a large installed base of mobile phones, tablets and computers.
- 5. Fully coverage of BLE software stack.
- 6. BLE & RF transmission switching helps products fit all operation system and most hardware.

### 1.1. Application

- · IoT
- Home automation
- Sensor networks
- Building automation
- Personal Area Networks
  - · Health / fitness sensor and monitor device
  - Medical devices
  - · Key-fobs and wrist watches
- Interactive entertainment devices
  - · Remote control
  - Gaming controller
- Beacons
- A4WP wireless chargers and devices
- Remote control toys
- Computer peripherals and I/O devices
  - Mouse
  - Keyboard
  - · Multi-touch trackpad

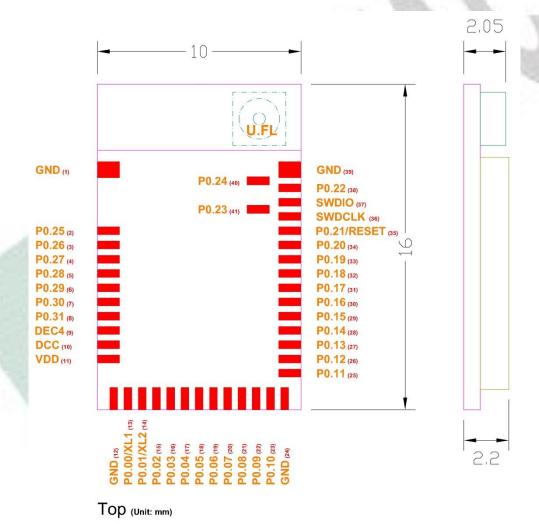
#### 1.2. Features

- Multi-protocol 2.4GHz radio
- 32-bit ARM Cortex M4F processor
- 512KB flash programmed memory and 64KB RAM
- Software stacks available as downloads
- Application development independent from protocol stack
- · On-air compatible with nRF51, nRF24AP and nRF24L series
- Programmable output power from +4dBm to -20dBm
- · RSSI
- RAM mapped FIFOs using EasyDMA
- · Dynamic on-air payload length up to 256 bytes
- · Flexible and configurable 32 pin GPIO
- · Programmable peripheral interface PPI
- Simple ON / OFF global power mode
- · Full set of digital interface all with Easy DMA including:
  - 3 x Hardware SPI master; 3 x Hardware SPI slave
  - 2 x two-wire master; 2 x two-wire slave
  - 1 x UART (CTS / RTS)
  - · PDM for digital microphone
  - · I2S for audio
- Quadrature demodulator
- 12-bit / 200KSPS ADC
- 128-bit AES ECB / CCM / AAR co-processor
- Low cost external crystal 32MHz ± 40ppm for Bluetooth; ± 50ppm for ANT Plus
- Low power 32MHz crystal and RC oscillators
- Wide supply voltage range 1.7V to 3.6V
- On-chip DC/DC buck converter
- Individual power management for all peripherals
- Timer counter
  - 5 x 32-bit
  - 3 x 24-bit RTC
- Type 2 near field communication (NFC-A) tag with wakeup-on-field and touch-to-pair capabilities
- · 3x 4-channel pulse width modulator (PWM) units with EasyDMA

# 2. Product Dimension

#### 2.1. PCB Dimensions & Pin Indication

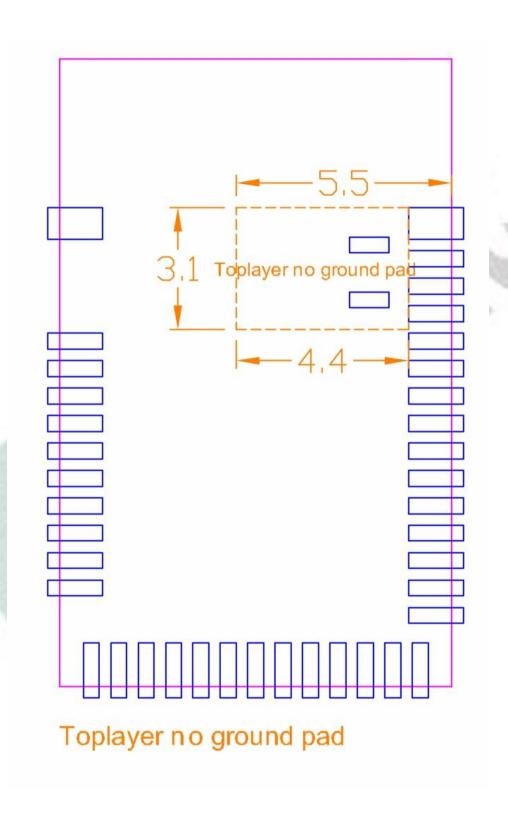
PCB Size (in mm)				
Min. Norm MAX.			MAX.	
L		16		
W	- 0.15	10	+ 0.2	
Н	_	2.2	-	

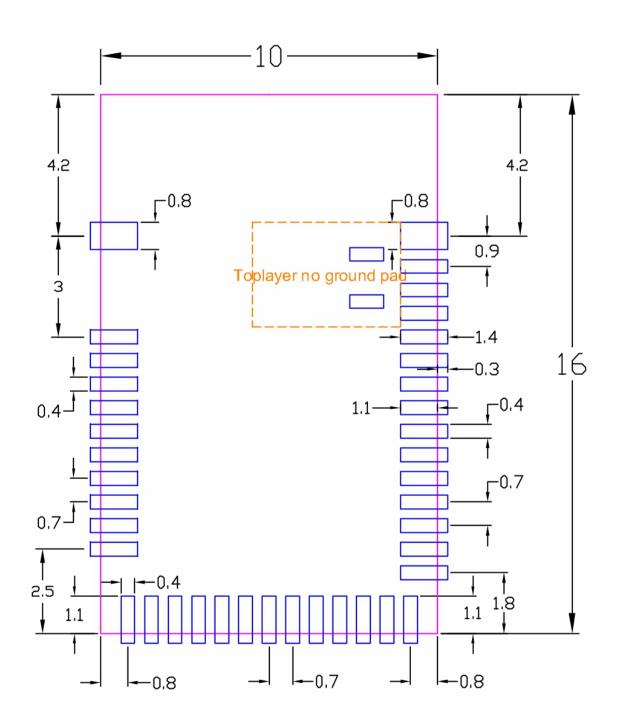


<sup>\*</sup> Please be careful of the amount of solder paste for P0.23 & P0.24. The module may be lifted due to excess solder. Pads for P0.23 & P0.24 can be omitted when two GPIOs were not used.

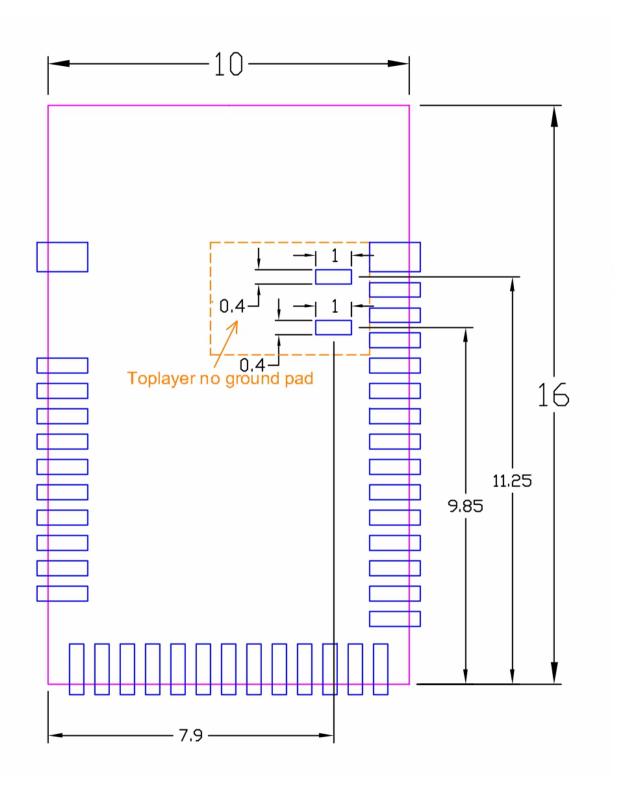
# 2.2. Recommended Layout of Solder Pad

Graphs are all in Top View, Unit in mm.





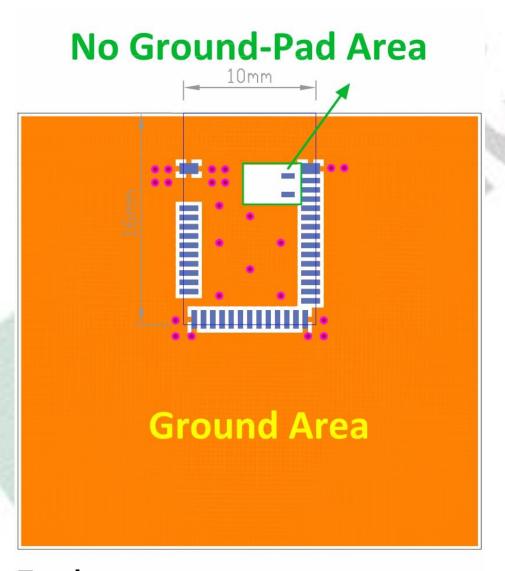
Top View (單位:mm) recommended solder pad layout



### 2.3. RF Layout Suggestion (aka Keep-Out Area)

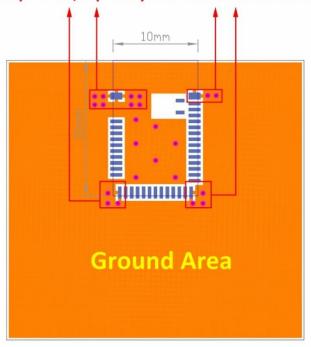
Please follow below instruction to avoid from having Ground Pad in the area of our RF test pad. Otherwise, it may cause shortage to the module.

Welcome to send us your layout in PDF for review at <a href="service@raytac.com">service@raytac.com</a> or your contact at Raytac with title <a href="service@raytac.com">Layout reviewing – Raytac model no. – YOUR company's name</a>.

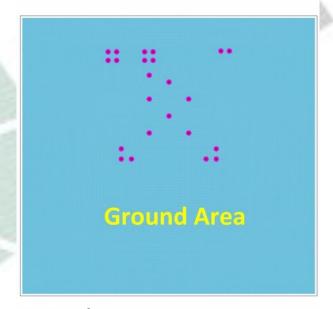


**Top layer** 

Please add via holes in GROUND area as many as possible, especially around the four corners.



**Top layer** 



**Bottom layer** 

# 2.4. Footprint & Design Guide

Please visit "Support" page of our website to download. The package includes footprint, 2D/3D drawing, reflow graph and recommended spec for external 32.768khz.

# 2.5. Pin Assignment

Pin No.	Name	Pin function	Description	
(1)	GND	Ground	The pad must be connected to a solid ground plane	
(2)	P0.25	Digital I/O	General-purpose digital I/O	
(3)	P0.26	Digital I/O	General-purpose digital I/O	
(4)	P0.27	Digital I/O	General-purpose digital I/O	
(E)	P0.28	Digital I/O	General-purpose digital I/O	
(5)	AIN4	Analog input	SAADC/COMP/LPCOMP input	
(6)	P0.29	Digital I/O	General-purpose digital I/O	
(6)	AIN5	Analog input	SAADC/COMP/LPCOMP input	
( <del>7</del> )	P0.30	Digital I/O	General-purpose digital I/O	
(7)	AIN6	Analog input	SAADC/COMP/LPCOMP input	
/o\	P0.31	Digital I/O	General-purpose digital I/O	
(8)	AIN7	Analog input	SAADC/COMP/LPCOMP input	
(9)	DEC4	Power	1V3 regulator supply decoupling. Input from DC/DC	
(10)	DCC	Power	DC/DC converter output pin	
(11)	VDD	Power	Power-supply pin	
(12)	GND	Ground	The pad must be connected to a solid ground plane	
(40)	P0.00	Digital I/O	General-purpose digital I/O	
(13)	XL1	Analog input	Connection to 32.768khz crystal (LFXO)	
(4.4)	P0.01	Digital I/O	General-purpose digital I/O	
(14)	XL2	Analog input	Connection to 32.768khz crystal (LFXO)	
(4.5)	P0.02	Digital I/O	General-purpose digital I/O	
(15)	AIN0	Analog input	SAADC/COMP/LPCOMP input	
(4.0)	P0.03	Digital I/O	General-purpose digital I/O	
(16)	AIN1	Analog input	SAADC/COMP/LPCOMP input	
/4 <b>-</b> >	P0.04	Digital I/O	General-purpose digital I/O	
(17)	AIN2	Analog input	SAADC/COMP/LPCOMP input	
(10)	P0.05	Digital I/O	General-purpose digital I/O	
(18)	AIN3	Analog input	SAADC/COMP/LPCOMP input	
(19)	P0.06	Digital I/O	General-purpose digital I/O	
(20)	P0.07	Digital I/O	General-purpose digital I/O	

P0.08	Pin No.	Name	Pin function	Description	
NFC1   NFC input   NFC antenna connection	(21)	P0.08	Digital I/O	General-purpose digital I/O	
NFC1	(22)	P0.09	Digital I/O	General-purpose digital I/O	
NFC2	(22)	NFC1	NFC input	NFC antenna connection	
NFC2   NFC input   NFC antenna connection	(22)	P0.10	Digital I/O	General-purpose digital I/O	
P0.11   Digital I/O   General-purpose digital I/O	(23)	NFC2	NFC input	NFC antenna connection	
	(24)	GND	Ground	The pad must be connected to a solid ground plane	
P0.13   Digital I/O   General-purpose digital I/O	(25)	P0.11	Digital I/O	General-purpose digital I/O	
P0.14   Digital I/O   General-purpose digital I/O	(26)	P0.12	Digital I/O	General-purpose digital I/O	
TraceData(3) Trace port output  (29) P0.15 Digital I/O General-purpose digital I/O  TraceData(2) Trace port output  (30) P0.16 Digital I/O General-purpose digital I/O  TraceData(1) Trace port output  (31) P0.17 Digital I/O General-purpose digital I/O  (32) P0.18 Digital I/O General-purpose digital I/O  TraceData(0) Trace port output  (33) P0.19 Digital I/O General-purpose digital I/O  (34) P0.20 Digital I/O General-purpose digital I/O  TraceCLK Trace port clock output  (35) P0.21 Digital I/O General-purpose digital I/O  RESET Configurable as system RESET pin  (36) SWDCLK Digital input Serial Wire debug clock input for debug and programming  (37) SWDIO Digital I/O General-purpose digital I/O  Serial Wire debug I/O for debug and programming  (38) P0.22 Digital I/O General-purpose digital I/O  The pad must be connected to a solid ground plane  (40) P0.24 Digital I/O General-purpose digital I/O	(27)	P0.13	Digital I/O	General-purpose digital I/O	
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(35)  RESET  Configurable as system RESET pin  (36)  SWDCLK  Digital input  Serial Wire debug clock input for debug and programming  (37)  SWDIO  Digital I/O  Serial Wire debug I/O for debug and programming  (38)  P0.22  Digital I/O  General-purpose digital I/O  (39)  GND  Ground  The pad must be connected to a solid ground plane  (40)  P0.24  Digital I/O  General-purpose digital I/O	(34)	TraceCLK		Trace port clock output	
RESET Configurable as system RESET pin  (36) SWDCLK Digital input Serial Wire debug clock input for debug and programming  (37) SWDIO Digital I/O Serial Wire debug I/O for debug and programming  (38) P0.22 Digital I/O General-purpose digital I/O  (39) GND Ground The pad must be connected to a solid ground plane  (40) P0.24 Digital I/O General-purpose digital I/O	(25)	P0.21	Digital I/O	General-purpose digital I/O	
(37) SWDIO Digital I/O Serial Wire debug I/O for debug and programming  (38) P0.22 Digital I/O General-purpose digital I/O  (39) GND Ground The pad must be connected to a solid ground plane  (40) P0.24 Digital I/O General-purpose digital I/O	(33)	RESET		Configurable as system RESET pin	
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(40) P0.24 Digital I/O General-purpose digital I/O	(38)	P0.22	Digital I/O	gital I/O General-purpose digital I/O	
	(39)	GND	Ground	The pad must be connected to a solid ground plane	
(41) P0.23 Digital I/O General-purpose digital I/O	(40)	P0.24	Digital I/O	General-purpose digital I/O	
	(41)	P0.23 Digital I/O		General-purpose digital I/O	

#### 2.6. GPIO Located Near the Radio

Some GPIO have recommended usage. To maximize RF performance, these GPIO are only available to use as **low drive**, **low frequency I/O only**. Wrong usage may lead to undesirable performance. Here is the list of these GPIO:

Low frequency I/O is a signal with a frequency up to 10 kHz. SPI, I2C, UART, PWM are NOT low frequency I/O.

Module Pin	GPIO
38	P0.22
41	P0.23
40	P0.24
2	P0.25
3	P0.26
4	P0.27
5	P0.28
6	P0.29
7	P0.30

# 3. Main Chip Solution

RF IC	Crystal Frequency
Nordic NRF52832	32MHZ

32MHz crystal is already inside the module.

# 4. Shipment Packaging Information

Model	Antenna	Photo
MDBT42Q-U512KV2	u.FL Connector	

- Unit Weight of Module:

MDBT42Q-U512KV2:  $0.64g / pc (\pm 0.02g)$ 

- Packaging Type: Anti-static Tray or Tape & Reel.

	Tray	Tape & Reel
MPQ (Min. Package Q'ty)	88 pcs per tray	1,056 pcs per reel
Carton Contents (per carton)	1,760 pcs	1,056 pcs
Carton Dimension (L) x (W) x (H) cm	37 x 21 x 13	37 x 36 x 6
Gross Weight	about 2.8 kgs	about 1.5 kgs

### 4.1. Marking on Metal Shield

#### 4.1.1. Label

Label context for date code 939 and after:



Before date code 939

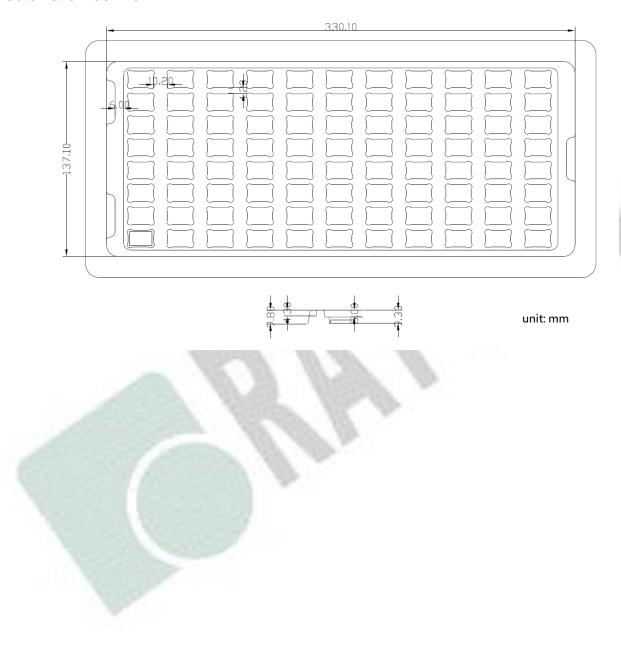
# 4.1.2. Dot Marking

Dot	Date Code	IC Revision	Photo
Yellow	813 ~ 950	2	
None	001 (starts from 2020)	2	

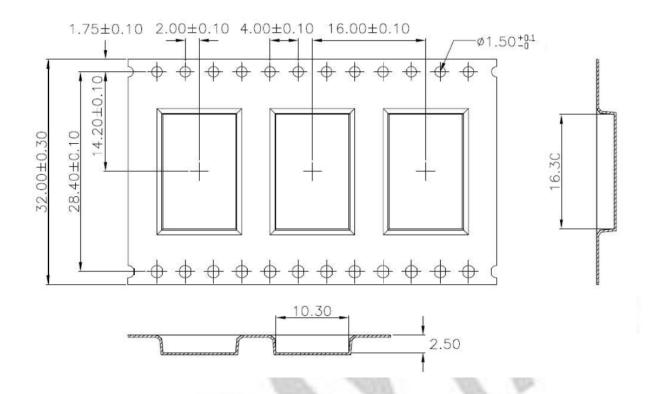
# 4.2. Packaging Info

# 4.2.1. Tray Packaging

Anti-static tray is specifically designed for mass production. It can be used directly on SMT automatic machine.



# 4.2.2. Reel Packaging



W	32.00 ±0.30
P1	16.00 ±0.10
E1	1.75 ±0.10
F	14.20 ±0.10
D0	1.50 +0.1/-0
P0	4.00 ±0.10
P2	2.00 ±0.10
S0	28.40 ±0.10
A0	10.30 ±0.10
B0	16.30 ±0.10
K0	2.50 ±0.10
T	0.30 ±0.05

### 4.3. Order Code

The model has two options of packaging. Please use following part no. when placing order to us.

Model	Tray	Tape & Reel
MDBT42Q-U512KV2	MD-240A2-S51	MD-240A2-S51R

MPQ of Reel packaging is 1,056 pcs and Tray packaging is 88 pcs.



# 5. Specification

Any technical spec shall refer to Nordic's official documents as final reference. Contents below are from "nRF52832 Product Specification v1.4", please click to download full spec.

# 5.1. Absolute Maximum Ratings

	Min.	Max.	Unit
Supply voltages			
VDD	-0.3	+3.9	V
VSS		0	V
I/O pin voltage			
V <sub>I/O</sub> , VDD ≤3.6 V	-0.3	VDD + 0.3 V	V
V <sub>I/O</sub> , VDD >3.6 V	-0.3	3.9 V	V
NFC antenna pin current			
I <sub>NFC1/2</sub>		80	mA
Radio			
RF input level		10	dBm
Environmental QFN48, 6×6 mm package			
Storage temperature	-40	+125	°C
MSL (moisture sensitivity level)		2	
ESD HBM (human body model)		4	kV
ESD CDM (charged device model)		1000	V
Flash memory			
Endurance	10 000		Write/erase cycles
Retention	10 years at 40°C		

# 5.2. Operation Conditions

Symbol	Parameter	Min.	Nom.	Max.	Units
VDD	Supply voltage, independent of DCDC enable	1.7	3.0	3.6	V
$t_{R\_VDD}$	Supply rise time (0 V to 1.7 V)			60	ms
TA	Operating temperature	-40	25	85	°C

**Important:** The on-chip power-on set circuitry may not function properly for rise times longer than the specified maximum.

# 5.3. Electrical Specifications

# 5.3.1. General Radio Characteristics

Symbol	Description	Min.	Тур.	Max.	Units
$f_{OP}$	Operating frequencies	2360		2500	MHz
f <sub>PLL,PROG,RES</sub>	PLL programming resolution		2		kHz
f <sub>PLL,CH,SP</sub>	PLL channel spacing		1		MHz
f <sub>DELTA,1M</sub>	Frequency deviation @ 1 Msps		±170		kHz
f <sub>DELTA,BLE,1M</sub>	Frequency deviation @ BLE 1Msps		±250		kHz
f <sub>DELTA,2M</sub>	Frequency deviation @ 2 Msps		±320		kHz
f <sub>DELTA,BLE,2M</sub>	Frequency deviation @ BLE 2 Msps		±500		kHz
fsk <sub>SPS</sub>	On-the-air data rate	1		2	Msps

# 5.3.2. Radio Current Consumption (Transmitter)

Symbol	Description	Min.	Тур.	Max.	Units
I <sub>TX,PLUS4dBM,DCDC</sub>	TX only run current (DCDC, 3V) P <sub>RF</sub> =+4 dBm		7.5		mA
I <sub>TX,PLUS4dBM</sub>	TX only run current P <sub>RF</sub> = +4 dBm		16.6		mA
I <sub>TX,0dBM,DCDC</sub>	TX only run current (DCDC, 3V)P <sub>RF</sub> = 0dBm		5.3		mA
I <sub>TX,0dBM</sub>	TX only run current P <sub>RF</sub> = 0dBm		11.6		mA
I <sub>TX,MINUS4dBM,DCDC</sub>	TX only run current DCDC, 3V P <sub>RF</sub> = -4dBm		4.2		mA
I <sub>TX,MINUS4dBM</sub>	TX only run current P <sub>RF</sub> = -4 dBm		9.3		mA
I <sub>TX,MINUS8dBM,DCDC</sub>	TX only run current DCDC, 3V P <sub>RF</sub> = -8 dBm		3.8		mA
I <sub>TX,MINUS8dBM</sub>	TX only run current P <sub>RF</sub> = -8 dBm		8.4		mA
I <sub>TX,MINUS12dBM,DCDC</sub>	TX only run current DCDC, 3V $P_{RF}$ = -12 dBm		3.5		mA
I <sub>TX,MINUS12dBM</sub>	TX only run current P <sub>RF</sub> = -12 dBm		7.7		mA
I <sub>TX,MINUS16dBM,DCDC</sub>	TX only run current DCDC, 3V $P_{RF}$ = -16 dBm		3.3		mA
I <sub>TX,MINUS16dBM</sub>	TX only run current P <sub>RF</sub> = -16 dBm		7.3		mA
I <sub>TX,MINUS20dBM,DCDC</sub>	TX only run current DCDC, 3V P <sub>RF</sub> = -20 dBm		3.2		mA
I <sub>TX,MINUS20dBM</sub>	TX only run current P <sub>RF</sub> = -20 dBm		7.0		mA
I <sub>TX,MINUS40dBM,DCDC</sub>	TX only run current DCDC, 3V P <sub>RF</sub> = -40 dBm		2.7		mA
I <sub>TX,MINUS40dBM</sub>	TX only run current P <sub>RF</sub> = -40 dBm		5.9		mA
I <sub>START,TX,DCDC</sub>	TX start-up current DCDC, 3V, P <sub>RF</sub> = 4 dBm		4.0		mA
I <sub>START,TX</sub>	TX start-up current, P <sub>RF</sub> = 4 dBm		8.8		mA
SIANI,IA					

# 5.3.3. Radio Current Consumption (Receiver)

Symbol	Description	Min.	Тур.	Max.	Units
I <sub>RX,1M,DCDC</sub>	RX only run current (DCDC, 3V) 1Msps / 1Msps BLE		5.4		mA
I <sub>RX,1M</sub>	RX only run current 1Msps / 1Msps BLE		11.7		mA
I <sub>RX,2M,DCDC</sub>	RX only run current (DCDC, 3V) 2Msps / 2Msps BLE		5.8		mA
I <sub>RX,2M</sub>	RX only run current 2Msps / 2Msps BLE		12.9		mA
I <sub>START,RX,DCDC</sub>	RX start-up current (DCDC 3V)		3.5		mA
I <sub>START,RX,LDO</sub>	RX start-up current (LDO 3V)		7.5		mA

# 5.3.4. Transmitter Specification

Symbol	Description	Min.	Тур.	Max.	Units
P <sub>RF</sub>	Maximum output power		4	6	dBm
P <sub>RFC</sub>	RF power control range		24		dB
$P_{RFCR}$	RF power accuracy			±4	dB
P <sub>RF1,1</sub>	1st Adjacent Channel Transmit Power 1 MHz (1 Msps Nordic proprietary mode)		-25		dBc
P <sub>RF2,1</sub>	2nd Adjacent Channel Transmit Power 2 MHz (1 Msps Nordic proprietary mode)		-50		dBc
P <sub>RF1,2</sub>	1st Adjacent Channel Transmit Power 2 MHz (2 Msps Nordic proprietary mode)		-25		dBc
P <sub>RF2,2</sub>	2nd Adjacent Channel Transmit Power 4 MHz (2 Msps Nordic proprietary mode)		-50		dBc
P <sub>RF1,2,BLE</sub>	1st Adjacent Channel Transmit Power 2 MHz (2 Msps BLE mode)		-20		dBc
P <sub>RF2,2,BLE</sub>	2nd Adjacent Channel Transmit Power 4 MHz (2 Msps BLE mode)		-50		dBc

# 5.3.5. Receiver Operation

Symbol	Description	Min.	Тур.	Max.	Units
P <sub>RX,MAX</sub>	Maximum received signal strength at < 0.1% BER		0		dBm
P <sub>SENS,IT,1M</sub>	Sensitivity, 1Msps nRF mode <sup>1</sup>		-93		dBm
P <sub>SENS,IT,SP,1M,BLE</sub>	Sensitivity, 1Msps BLE ideal transmitter, <=37 bytes BER=1E-3 <sup>2</sup>		-96		dBm
P <sub>SENS,IT,LP,1M,BLE</sub>	Sensitivity, 1Msps BLE ideal transmitter >=128 bytes BER=1E-4 <sup>3</sup>		-95		dBm
P <sub>SENS,IT,2M</sub>	Sensitivity, 2Msps nRF mode <sup>4</sup>		-89		dBm
P <sub>SENS,IT,SP,2M,BLE</sub>	Sensitivity, 2Msps BLE ideal transmitter, Packet length		-93		dBm
	<=37bytes				

<sup>1.</sup> Typical sensitivity applies when ADDR0 is used for receiver address correlation. When ADDR [1...7] are used for receiver address correlation, the typical sensitivity for this mode is degraded by 3dB.

<sup>2.</sup> As defined in the Bluetooth Core Specification v4.0 Volume 6: Core System Package (Low Energy Controller Volume).

<sup>3.</sup> Equivalent BER limit < 10E-04.

<sup>4.</sup> Same as remark 1.

Symbol	Description	Min.	Тур.	Max.	Units
P <sub>SENS,DT,SP,2M,BLE</sub>	Sensitivity, 2Msps BLE dirty transmitter, Packet length		-93		dBm
	<=37bytes				
P <sub>SENS,IT,LP,2M,BLE</sub>	Sensitivity, 2Msps BLE ideal transmitter >= 128bytes		-92		dBm
P <sub>SENS,DT,LP,2M,BLE</sub>	Sensitivity, 2Msps BLE dirty transmitter, Packet length >=		-92		dBm
	128bytes				

# 5.3.6. RX Selectivity

Symbol	Description	Min.	Тур.	Max.	Units
C/I <sub>1M,co-channel</sub>	1Msps mode, Co-Channel interference		9		dB
C/I <sub>1M,-1MHz</sub>	1 Msps mode, Adjacent (-1 MHz) interference		-2		dB
C/I <sub>1M,+1MHz</sub>	1 Msps mode, Adjacent (+1 MHz) interference		-10		dB
C/I <sub>1M,-2MHz</sub>	1 Msps mode, Adjacent (-2 MHz) interference		-19		dB
C/I <sub>1M,+2MHz</sub>	1 Msps mode, Adjacent (+2 MHz) interference		-42		dB
C/I <sub>1M,-3MHz</sub>	1 Msps mode, Adjacent (-3 MHz) interference		-38		dB
C/I <sub>1M,+3MHz</sub>	1 Msps mode, Adjacent (+3 MHz) interference		-48		dB
C/I <sub>1M,±6MHz</sub>	1 Msps mode, Adjacent (≥6 MHz) interference		-50		dB
C/I <sub>1MBLE,co-channel</sub>	1 Msps BLE mode, Co-Channel interference		6		dB
C/I <sub>1MBLE,-1MHz</sub>	1 Msps BLE mode, Adjacent (-1 MHz) interference		-2		dB
C/I <sub>1MBLE,+1MHz</sub>	1 Msps BLE mode, Adjacent (+1 MHz) interference		-9		dB
C/I <sub>1MBLE,-2MHz</sub>	1 Msps BLE mode, Adjacent (-2 MHz) interference		-22		dB
C/I <sub>1MBLE,+2MHz</sub>	1 Msps BLE mode, Adjacent (+2 MHz) interference		-46		dB
C/I <sub>1MBLE,&gt;3MHz</sub>	1 Msps BLE mode, Adjacent (≥3 MHz) interference		-50		dB
C/I <sub>1MBLE,image</sub>	Image frequency Interference		-22		dB
C/I <sub>1MBLE,image,1MHz</sub>	Adjacent (1 MHz) interference to in-band image frequency		-35		dB
C/I <sub>2M,co-channel</sub>	2Msps mode, Co-Channel interference		10		dB
C/I <sub>2M,-2MHz</sub>	2 Msps mode, Adjacent (-2 MHz) interference		6		dB
C/I <sub>2M,+2MHz</sub>	2 Msps mode, Adjacent (+2 MHz) interference		-14		dB
C/I <sub>2M,-4MHz</sub>	2 Msps mode, Adjacent (-4 MHz) interference		-20		dB
C/I <sub>2M,+4MHz</sub>	2 Msps mode, Adjacent (+4 MHz) interference		-44		dB
C/I <sub>2M,-6MHz</sub>	2 Msps mode, Adjacent (-6 MHz) interference		-42		dB
C/I <sub>2M,+6MHz</sub>	2 Msps mode, Adjacent (+6 MHz) interference		-47		dB
C/I <sub>2M,≥12MHz</sub>	2 Msps mode, Adjacent (≥12 MHz) interference		-52		dB
C/I <sub>2MBLE,co-channel</sub>	2 Msps BLE mode, Co-Channel interference		7		dB
C/I <sub>2MBLE,±2MHz</sub>	2 Msps BLE mode, Adjacent (±2 MHz) interference		0		dB
C/I <sub>2MBLE,±4MHz</sub>	2 Msps BLE mode, Adjacent (±4 MHz) interference		-47		dB
C/I <sub>2MBLE,≥6MHz</sub>	2 Msps BLE mode, Adjacent (≥6 MHz) interference		-49		dB
$\mathrm{C/I}_{\mathrm{2MBLE,image}}$	Image frequency Interference		-21		dB
C/I <sub>2MBLE,image, 2MHz</sub>	Adjacent (2 MHz) interference to in-band image frequency		-36		dB

Remark: Wanted signal level at PIN = -67 dBm. One interferer is used, having equal modulation as the wanted signal. The input power of the interferer where the sensitivity equals BER = 0.1% is presented.

### 5.3.7. RX Intermodulation

Symbol	Description	Min.	Тур.	Max.	Units
P <sub>IMD,1M</sub>	IMD performance, 1 Msps (3 MHz, 4 MHz, and 5 MHz offset)		-33		dBm
P <sub>IMD,1M,BLE</sub>	IMD performance, BLE 1 Msps (3 MHz, 4 MHz, and 5 MHz offset)		-30		dBm
P <sub>IMD,2M</sub>	IMD performance, 2 Msps (6 MHz, 8 MHz, and 10 MHz offset)		-33		dBm
P <sub>IMD,2M,BLE</sub>	IMD performance, BLE 2 Msps (6 MHz, 8 MHz, and 10 MHz offset)		-32		dBm

Remark: Wanted signal level at PIN = -64dBm. Two interferers with equal input power are used. The interferer closet in frequency is not modulated, the other interferer is modulated equal with the wanted signal. The input power of the interferers where the sensitivity equals BER = 0.1% is presented.

# 5.3.8. Radio Timing Parameters

Time between TXEN task and READY event after channel  FREQUENCY configured  trxen, FAST Time between TXEN task and READY event after channel FREQUENCY configured (Fast Mode)  trxDISABLE Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 1Msps  trxDISABLE, Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  trxDISABLE, Time between the RXEN task and READY event after channel FREQUENCY configured in default mode  trxen, FAST Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode  trxen, FAST Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode  trypical Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode  trypical Time between the RXEN task and READY event after channel The minimum time taken to switch from RX to TX or TX to RX (channel FREQUENCY unchanged)  trxDISABLE Time between DISABLE task and DISABLED event when the radio was in RX  trxCHAIN TX chain delay  T						
FREQUENCY configured  taxen, Fast Time between TXEN task and READY event after channel FREQUENCY configured (Fast Mode)  taxen, Fast Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 1Msps  taxen Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  taxen Time between DISABLE task and READY event after channel frequency configured in default mode  taxen, Fast Time between the RXEN task and READY event after channel frequency configured in default mode  taxen, Fast Time between the RXEN task and READY event after channel frequency configured in fast mode  taxen, Fast Time between the RXEN task and READY event after channel frequency configured in fast mode  taxen, Fast Time between the RXEN task and READY event after channel frequency configured in fast mode  taxen, Fast Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between DISABLE task and DISABLED event when the radio was in RX  taxen, Time between D	Symbol	Description	Min.	Тур.	Max.	Units
Time between TXEN task and READY event after channel  FREQUENCY configured (Fast Mode)  Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 1Msps  Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  Time between the RXEN task and READY event after channel FREQUENCY configured in default mode  Trace Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode  Tome between the RXEN task and READY event after channel FREQUENCY configured in fast mode  Tome between the RXEN task and READY event after channel FREQUENCY configured in fast mode  Tome between DISABLE task and DISABLED event when the radio was in RX  Tome between DISABLE task and DISABLED event when the radio was in RX  TX chain delay	t <sub>TXEN</sub>	Time between TXEN task and READY event after channel		140		us
FREQUENCY configured (Fast Mode)  tandio was in TX and mode is set to 1Msps  tandio was in TX and mode is set to 1Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in TX and mode is set to 2Msps  tandio was in RX		FREQUENCY configured				
Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 1Msps  Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  Time between the RXEN task and READY event after channel freeQUENCY configured in default mode  Time between the RXEN task and READY event after channel freeQUENCY configured in fast mode  The minimum time taken to switch from RX to TX or TX to RX (channel FREQUENCY unchanged)  Time between DISABLE task and DISABLED event when the radio was in RX  TX chain delay	t <sub>TXEN,FAST</sub>	Time between TXEN task and READY event after channel		40		us
radio was in TX and mode is set to 1Msps  taxinisable, 2M Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  taxinisable, 2M Time between the RXEN task and READY event after channel FREQUENCY configured in default mode  taxinisable, 2M Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode  taxinisable, 2D Us (channel FREQUENCY configured in fast mode)  taxinisable, 2D Us (channel FREQUENCY unchanged)  taxinisable, 2D Us (channel FREQUENCY unchanged)  taxinisable, 2D Us Us (channel FREQUENCY unchanged)		FREQUENCY configured (Fast Mode)				
Time between DISABLE task and DISABLED event when the radio was in TX and mode is set to 2Msps  trace  trac	t <sub>TXDISABLE</sub>	Time between DISABLE task and DISABLED event when the		6		us
radio was in TX and mode is set to 2Msps  trender Time between the RXEN task and READY event after channel free properties of the properti		radio was in TX and mode is set to 1Msps				
Time between the RXEN task and READY event after channel FREQUENCY configured in default mode  trace Time between the RXEN task and READY event after channel FREQUENCY configured in fast mode  tswitch The minimum time taken to switch from RX to TX or TX to RX (channel FREQUENCY unchanged)  trace Time between DISABLE task and DISABLED event when the radio was in RX  trace Time between DISABLE task and DISABLED event when the RXEN task and DISABLED event when the radio was in RX  trace Time between DISABLE task and DISABLED event when the RXEN task and DISABLED event when t	t <sub>TXDISABLE,2M</sub>	Time between DISABLE task and DISABLED event when the		4		us
FREQUENCY configured in default mode  t <sub>RXEN,FAST</sub> Time between the RXEN task and READY event after channel 40 us  FREQUENCY configured in fast mode  t <sub>SWITCH</sub> The minimum time taken to switch from RX to TX or TX to RX 20 us  (channel FREQUENCY unchanged)  t <sub>RXDISABLE</sub> Time between DISABLE task and DISABLED event when the radio was in RX  t <sub>TXCHAIN</sub> TX chain delay 0.6 us  t <sub>RXCHAIN</sub> RX chain delay 9.4 us		radio was in TX and mode is set to 2Msps				
Time between the RXEN task and READY event after channel  FREQUENCY configured in fast mode  tswitch  The minimum time taken to switch from RX to TX or TX to RX  (channel FREQUENCY unchanged)  tRXDISABLE  Time between DISABLE task and DISABLED event when the radio was in RX  tTXCHAIN  TX chain delay  RX chain delay  RX chain delay  9.4  us	t <sub>RXEN</sub>	Time between the RXEN task and READY event after channel		140		us
FREQUENCY configured in fast mode  tswitch The minimum time taken to switch from RX to TX or TX to RX 20 us (channel FREQUENCY unchanged)  trxchain Time between DISABLE task and DISABLED event when the radio was in RX  trxchain TX chain delay 0.6 us  trxchain RX chain delay 9.4 us		FREQUENCY configured in default mode				
The minimum time taken to switch from RX to TX or TX to RX (channel FREQUENCY unchanged)  trime between DISABLE task and DISABLED event when the radio was in RX  trichain  TX chain delay  RX chain delay  RX chain delay  9.4  us	t <sub>RXEN,FAST</sub>	Time between the RXEN task and READY event after channel		40		us
(channel FREQUENCY unchanged)  t <sub>RXDISABLE</sub> Time between DISABLE task and DISABLED event when the radio was in RX  t <sub>TXCHAIN</sub> TX chain delay 0.6 us  t <sub>RXCHAIN</sub> RX chain delay 9.4 us		FREQUENCY configured in fast mode				
trachain Time between DISABLE task and DISABLED event when the radio was in RX  trachain TX chain delay 0.6 us  trachain RX chain delay 9.4 us	t <sub>SWITCH</sub>	The minimum time taken to switch from RX to TX or TX to RX		20		us
radio was in RX  t <sub>TXCHAIN</sub> TX chain delay 0.6 us  t <sub>RXCHAIN</sub> RX chain delay 9.4 us		(channel FREQUENCY unchanged)				
t <sub>TXCHAIN</sub> TX chain delay 0.6 us t <sub>RXCHAIN</sub> RX chain delay 9.4 us	t <sub>RXDISABLE</sub>	Time between DISABLE task and DISABLED event when the		0		us
t <sub>RXCHAIN</sub> RX chain delay 9.4 us		radio was in RX				
	t <sub>TXCHAIN</sub>	TX chain delay		0.6		us
t <sub>RXCHAIN,2M</sub> RX chain delay in 2Msps mode 5 us	t <sub>RXCHAIN</sub>	RX chain delay		9.4		us
	t <sub>RXCHAIN,2M</sub>	RX chain delay in 2Msps mode		5		us

# 5.3.9. RSSI Specifications

Symbol	Description	Min.	Тур.	Max.	Units
RSSI <sub>ACC</sub>	RSSI Accuracy Valid range -90 to -20 dBm		±2		dB
RSSI <sub>RESOLUTION</sub>	RSSI resolution		1		dB
RSSI <sub>PERIOD</sub>	Sample period		0.25		us

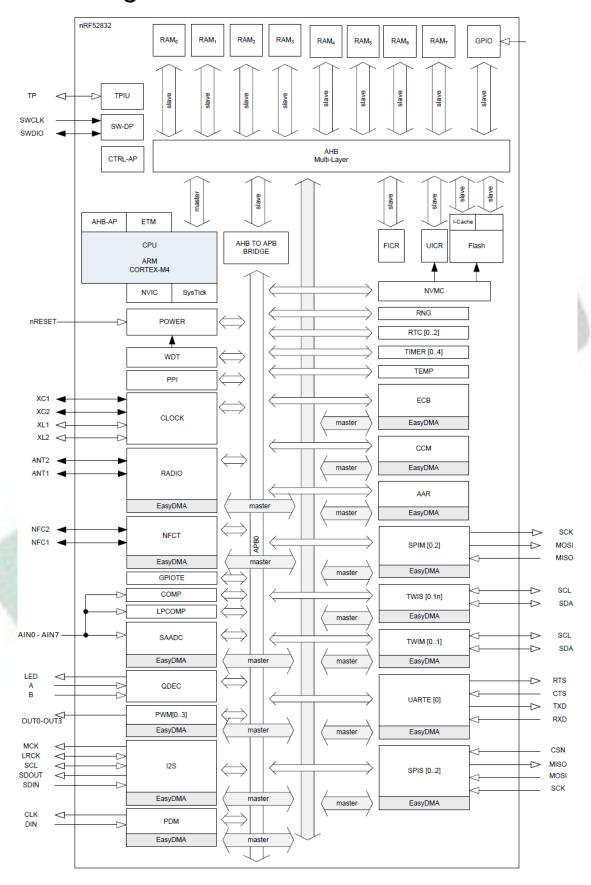
# 5.3.10. CPU

Symbol	Description	Min.	Тур.	Max.	Units
W <sub>FLASH</sub>	CPU wait states, running from flash, cache disabled	0		2	
W <sub>FLASHCACHE</sub>	CPU wait states, running from flash, cache enabled	0		3	
$W_{RAM}$	CPU wait states, running from RAM			0	
I <sub>DDFLASHCACHE</sub>	CPU current, running from flash, cache enabled, LDO		7.4		mA
I <sub>DDFLASHCACHEDCDC</sub>	CPU current, running from flash, cache enabled, DCDC 3V		3.7		mA
I <sub>DDFLASH</sub>	CPU current, running from flash, cache disabled, LDO		8.0		mA
I <sub>DDFLASHDCDC</sub>	CPU current, running from flash, cache disabled, DCDC 3V		3.9		mA
I <sub>DDRAM</sub>	CPU current, running from RAM, LDO		6.7		mA
I <sub>DDRAMDCDC</sub>	CPU current, running from RAM, DCDC 3V		3.3		mA
I <sub>DDFLASH/MHz</sub>	CPU efficiency, running from flash, cache enabled, LDO		125		μΑ/
					MHz
I <sub>DDFLASHDCDC/MHz</sub>	CPU efficiency, running from flash, cache enabled, DCDC 3V		58		μΑ/
CM <sub>FLASH</sub>	CoreMark <sup>5</sup> , running from flash, cache enabled		215		CoreN
CM <sub>FLASH/MHz</sub>	CoreMark per MHz, running from flash, cache enabled		3.36		CoreN
					MHz
CM <sub>FLASH/mA</sub>	CoreMark per mA, running from flash, cache enabled, DCDC 3V		58		CoreN
					mA

# 5.3.11. Power Management

Symbol	Description	Min.	Тур.	Max.	Units
I <sub>ON_RAMOFF_EVENT</sub>	System ON, No RAM retention, Wake on any event		1.2		μΑ
I <sub>ON_RAMON_EVENT</sub>	System ON, Full RAM retention, Wake on any event		1.5		μΑ
I <sub>ON_RAMOFF_RTC</sub>	System ON, No RAM retention, Wake on RTC		1.9		μΑ
I <sub>OFF_RAMOFF_RESET</sub>	System OFF, No RAM retention, Wake on reset		0.3		μΑ
I <sub>OFF_RAMOFF_GPIO</sub>	System OFF, No RAM retention, Wake on GPIO		0.3		μΑ
I <sub>OFF_RAMOFF_LPCOMP</sub>	System OFF, No RAM retention, Wake on LPCOMP		1.9		μΑ
I <sub>OFF_RAMOFF_NFC</sub>	System OFF, No RAM retention, Wake on NFC field		0.7		μΑ
I <sub>OFF_RAMON_RESET</sub>	System OFF, Full 64 kB RAM retention, Wake on reset		0.7		μΑ

# 6. Block Diagram



### 7. Antenna

Below chart shows a few options of external antenna which has been tested and approved to use with MDBT42Q-U512KV2. Please check <a href="https://example.com/9/certification">9 Certification</a> for details of antenna approval.

#	Photo	Model No.	Туре	Supplier	Gain	Spec	Certification
1		ANTX100 ETHAB24553	External	Yageo	≦ 2 dBi		FCC, IC, KC, TELEC, NCC, CE, SRRC
2	PAGES PAGES	ANTX200 P001B24003	РСВ	Yageo	4.4 dBi		FCC, IC, KC, TELEC, NCC, CE, SRRC
3	Total lopole forces in the party of the part	ANTX100 P111B24003	РСВ	Yageo	3.3 dBi		FCC, IC, KC, TELEC, NCC, CE, SRRC

<sup>\*</sup> End-product may need to perform further testing when applying CE and SRRC. Others are modular approval.

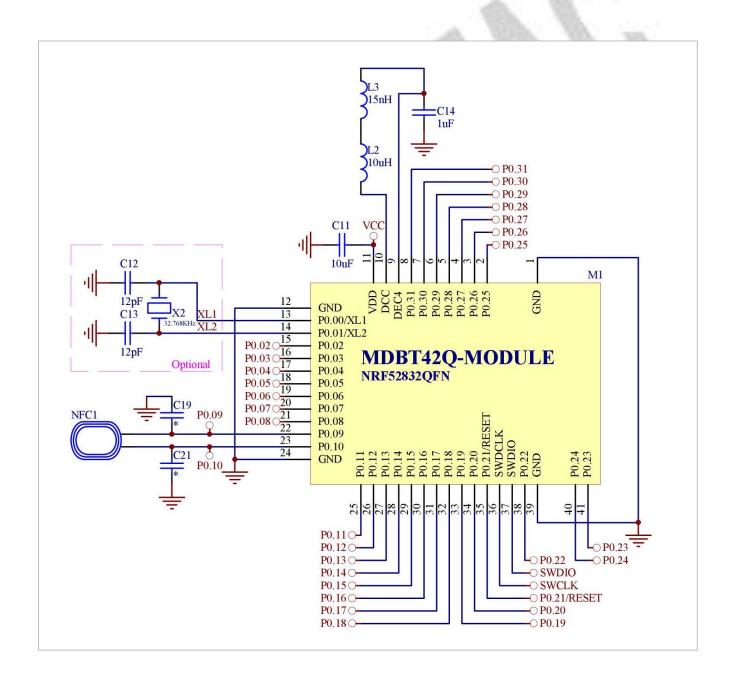
<sup>\*</sup> Our certificate(s) might not be applicable when using antenna apart from above list.

### 8. Reference Circuit

Module is pre-programmed with Raytac's testing code. Default is using "LDO mode". Our firmware is set to use external 32.768khz so please add it to make module work.

#### **REMARK:**

- \*\* DEC4 decoupling capacitor (1µF) is already inside the module. \*\*
- \*\* When using DC-DC mode, please add L2 / L3 / C14. \*\*
- \*\* When NOT using NFC, please remove NFC1 / C19 / C21. \*\*
- \*\* When using internal 32.768khz RC oscillator, please remove X2 / C12 / C13. \*\*



# 9. Certification

# 9.1. Declaration ID

# BT 4.2

Declaration ID	<b>\$</b>	QDID(s)	<b>\$</b>	Company	<b>\$</b>	Specification Name	<b>\$</b>
D033661		91882 - End Product		Raytac Corporation		4.2	

# <u>BT 5.0</u>

Declaration ID	<b>\$</b>	QDID(s)	<b>\$</b>	Company	<b>\$</b>	Specification Name	<b>\$</b>
D036781		100551 - End Product		Raytac Corporation		5.0	

# <u>BT 5.1</u>

Declaration ID	<b>\$</b>	QDID(s)	<b>\$</b>	Company	<b>\$</b>	Specification Name	<b>\$</b>
D047708		139361 - End Product		Raytac Corporation		5.1	

Profile Description	Service Description
Alert Notification Profile	Alert Notification Service
Dio ad Dragouro Drafila	Blood Pressure Service
Blood Pressure Profile	Device Information Service
Cycling Speed 9 Codence Drefile	Cycling Speed & Cadence Service
Cycling Speed & Cadence Profile	Device Information Service
Cluses Profile	Glucose Service
Glucose Profile	Device Information Service
Lloolth Thompson otor Drofile	Health Thermometer Service
Health Thermometer Profile	Device Information Service
Lloom Data Drafile	Heart Rate Service
Heart Rate Profile	Device Information Service
HID over CATT Profile	HID Service
HID over GATT Profile	Battery Service
	Link Loss Service
Proximity Profile	Immediate Alert Service
	TX Power Service
Dunning Chard & Cadanaa Duffla	Running Speed & Cadence Service
Running Speed & Cadence Profile	Device Information Service
Time Profile	Time Profile Service
Glucose Profile (Central)	
Mach Drofile	Mesh Provisioning Service
Mesh Profile	Mesh Proxy Service

### 9.2. FCC Certificate (USA)

#### BLE 4.2 & 5.0



**TCB** 

GRANT OF EQUIPMENT AUTHORIZATION

TCB

Certification Issued Under the Authority of the Federal Communications Commission

Telefication B.V. Edisonstraat 12a Zevenaar, NL-6902 PK Netherlands

Date of Grant: 05/29/2019

Application Dated: 05/24/2019

Raytac Corp. 5F., No.3, Jiankang Rd., Zhonghe Dist., New Taipei City,, 23586 Taiwan

Attention: Venson Liao , R&D Manager

#### NOT TRANSFERABLE

EQUIPMENT AUTHORIZATION is hereby issued to the named GRANTEE, and is VALID ONLY for the equipment identified hereon for use under the Commission's Rules and Regulations listed below.

SH6MDBT42Q FCC IDENTIFIER: Name of Grantee: Raytac Corp.

Equipment Class:

Digital Transmission System BLE Module Notes:

Modular Type: Single Modular

Output Frequency Emission Watts Tolerance Designator Frequency Grant Notes FCC Rule Parts Range (MHZ)

15C 2402.0 - 2480.0 0.0023

Class 2 Permissive Change: This change is to add 3 antennas with different types and larger gain values and there is no any change in hardware or any relevant RF portion.

Power output listed is conducted. This grant is valid only when the module is sold to OEM integrators and must be installed by the OEM or OEM integrators. The antenna's as listed in this application must not be co-located or operating in conjunction with any other antenna or transmitter. End-users may not be provided with the module installation instructions. OEM integrators and end-users must be provided with transmitter operating conditions for satisfying RF exposure compliance.

Certificate No : 162181172/AA/03

Ramy Nabod Product Assessor

### 9.3. TELEC Certificate (Japan)

BLE 4.2 & 5.0

telefication by The Netherlands Chamber of Commerce 51565536 www.telefication.com



#### Certificate

Radio Equipment in JAPAN

No: 201-160496 / 02

Telefication, operating as Conformity Assessment Body (CAB ID Number: 201) with respect to Japan, declares that the listed product complies with the Technical Regulations Conformity Certification of Specified Radio equipment (ordinance of MPT N° 37, 1981)

> Product description: BLE Module Trademark: Raytac Type designation: MDBT42Q Hardware / Software version: 1 / 1 Variants: See Annex 3

> > Manufacturer: Raytac Corporation

Address: 5F, No. 3, Jiankang Rd., Zhonghe Dist.

City: 23586 New Taipei City

Country: Taiwan

This certificate is granted to:

Name: Raytac Corporation

Address: 5F, No. 3, Jiankang Rd., Zhonghe Dist. City: 23586 New Taipei City

Country: Taiwan

This certificate has THREE Annexes.

Zevenaar, 23 May 2019

CAB

David Chen Product Assessor

David Chen

## 9.4. NCC Certificate (Taiwan)

#### BLE 4.2 & 5.0

# 國家通訊傳播委員會電信管制射頻器材型式認證證明

一、申 請 者:勁達國際電子有限公司

二、地址:臺北市大安區和平東路1段145號5樓之1

三、製造廠商:勁達國際電子有限公司

四、 器 材 名 稱: BLE Module

五、廠 牌: Raytac

六、型 號: MDBT42Q-U

七、發射功率(電場強度):詳細射頻規格如備註欄

八、工作頻率:詳細射頻規格如備註欄

九、審驗日期: 108年08月01日

十、審驗合格標籤式樣:

€ CCAM16LP1182T3

國家通訊 傳播表 人名

十一、警語或標示要求:(器材本體、使用手冊、外包裝盒等應遵守下列標示要求)

- 應依審驗合格標籤或符合性聲明標籤式樣自製標籤黏贴或印鑄於電信管制射頻器材本體明顯 處,並於包裝盒標示本會標章,始得關陳列或販賣。
- 2. 電信管制射頻器材應依本會或相關技術規範規定於指定位置標示中文警語。
- 经授權使用射頻模組(組件)之審驗合格標籤者,應於最終產品說明書及包裝盒提供充分與正確之資訊。
- 於網際網路販賣取得審驗證明之電信管制射頻器材者,應於該網際網路網頁提供審驗合格標 籤或符合性聲明標籤資訊。
- 5. 使用手册應標示下列資訊:

(1)經型式認證合格之低功率射頻電機,非經許可,公司、商號或使用者均不得擅自變更頻率、加大功率或變更原設計之特性及功能。低功率射頻電機之使用不得影響稅航安全及干擾合法通信;經發現有干擾現象時,應立即停用,並改善至無干擾時方得繼續使用。前項合法通信,指依電信法規定作業之無線電通信。低功率射頻電機須忍受合法通信或工業、科學及醫療用電波輻射性電機設備之干擾。

6. 本器材之審驗範圍僅限無線射頻硬體功能,不及於器材之資通安全檢測。

型式認證號碼:CCAM16LP1182T3 第 1 頁,共 2 頁 本證書與贖頁分開使用無效

# 9.5. CE Test Report (EU)

#### BLE 4.2 & 5.0



Report No.: ER/2016/70008-02

Page: 2 of 60

VERIFICATION OF COMPLIANCE

Applicant: Raytac Corporation

5F, No.3, Jiankang Rd., Zhonghe Dist., New Taipei City, 23586,

Taiwan

Product Name: BLE Module

Brand Name: Raytac

Model No.: MDBT42Q-U

Model Difference: N/A

File Number: ER/2017/70008-02

Date of test: Apr. 17, 2019 ~ May 10, 2019

Date of EUT Received: Apr. 17, 2019

APPLICABLE STANDARDS						
STANDARD	TEST RESULT					
ETSI EN 300 328 v2.1.1: 2016	Complied					

The above equipment was tested by SGS Taiwan Ltd., Electronics & Communication Laboratory for compliance with the requirements set forth in the European Standard ETSI EN 300 328 v2.1.1: 2016 under RED 2014/53/EU Class II. The results of testing in this report apply to the product system that was tested only. Other similar equipment will not necessarily produce the same results due to production tolerance and measurement uncertainties.

-	14.	~	W 1
			ν.

Date: May 15, 2019

Kevin Shih / Engineer

Prepared By:

Test By:

Date:

May 15, 2019

Tiffany Kao / Clerk

May 15, 2019

Chun Chieh Chen / Asst. Supervisor

No.134,WuKungRoad,NewTaipelindustriaPark,WukuDistrict,NewTaipelCity,Taiwan24803/新北市五股區新北產業園區五工路134 號

台灣機能并技能会有限公司 t (886-2) 2299-3279

f (888-2) 2298-0488

www.tw.sgs.com Member of SGS Group



SGS Reference No: MH/2019/40103C

#### VERIFICATION OF EMC COMPLIANCE

: MH/2019/40103C Verification No.

Representative Model No. : MDBT42Q

Added Model(s) : MDBT42Q-P, MDBT42Q-U

Product Name **BLE Module** Brand Name : Raytac

Applicant : Raytac Corporation

Address of Applicant 5F, No.3, Jiankang Rd., Zhonghe Dist., New Taipei City, 23586, Taiwan

Test Report Number : MH/2019/40103 Date of Issue : May 13, 2019

Applicable Standards : EN 301 489 -1 v220: 2017-03 (Draft)

EN 301 489 -17 v3.20: 2017-03 (Draft)

EN 55032: 2015+AC:2016-07

EN 61000-4-2: 2009, EN 61000-4-3: 2006+A1:2008+A2:2010

<u>Conclusion</u>
The apparatus meets the requirements of the above standards and hence compliance the essential requirements under article 3.1b of the RED (2014/53/EU) Directive.

\*This verification is only valid for the equipment and configuration described, and in conjunction with the test report as detailed above.

Authorized Signatory:

SGS TAIWAN LTD. Eddy Cheng

Technical Asst. Supervisor

### 9.6. IC Certificate (Canada)

BLE 4.2 & 5.0

telefication by The Netherlands Chamber of Commerce 51565536



www.telefication.com

#### TECHNICAL ACCEPTANCE CERTIFICATE

CERTIFICAT D'ACCEPTABILITÉ TECHNIQUE

CERTIFICATION No. No. DE CERTIFICATION

8017A-MDBT42Q

TELEFICATION No. No. DE TELEFICATION

162170280/AA/03

TEST SITE No.

2324G

ISSUED TO

Raytac Corporation

DÉLIVRÉ A

TYPE OF EQUIPMENT GENRE DE MATÉRIEL

No. DE LABORATOIRE

Bluetooth device

TRADE NAME AND MODEL MARQUE ET MODELE

Raytac / MDBT42Q-U

CERTIFIED TO CERTIFIÉ SELON LE

SPECIFICATION CAHIER DES CHARGES

RSS-102 RSS-247 ISSUE

Certification of equipment means only that the equipment has met the requirements of the above-noted specification. Licence applications, where applicable to use certified equipment, are acted on accordingly by the ISED issuing office and will depend on the existing radio environment, service and location of operation. This certificate is issued on condition that the holder complies and will continue to comply with the requirements and procedures issued by ISED. The where applicable to use certified equipment, are acted on according by the ISED issuing office and will depend on the existing radio environment, service and location of operation. This certificate is issued on condition that the holder complies and will continue to comply with the requirements and procedures issued by ISED. The equipment for which this certificate is issued shall not be manufactured, imported, distributed, leased, offered for sale or sold unless the equipment complies with the applicable technical specifications and procedures issued by ISED.

titulaire satisfasse et continue de satisfaire aux exigences et aux procédures d'ISDE. Le matériel à l'égard duquel le présent certificat est délivre ne doit pas être fabrique, importé, distribué, loué, mis en vente ou procédures d'aux procédures et aux procédires et aux expériés etients. vendu à moins d'être conforme aux procédures et aux spécifications techniques applicables publiées par ISDE.

ISSUED BY TELEFICATION BV (NL0001), RECO<mark>GNIZED C</mark>ERTIFICATION BODY BY INNOVATION, SCIENCE AND ECONOMIC DEVELOPMENT CANADA DELIVRÉ PAR TELEFICATION BV (NL0001), ORGANISME DE CERTIFICATION RECONNU PAR INNOVATION, SCIENCES ET DEVELOPPEMENT ECONOMIQUE CANADA

I hereby attest that the subject equipment was tested and found in compliance with the above-noted specification. J'atteste, par la présente, que le matériel a fait l'objet d'essai et a été jugé conforme à la spécification ci-dessus

DATE 30 May 2019 BY

George Chen Manager Taiwan

This certificate has one annex.

#### SRRC Certificate (China) 9.7.

BLE 4.2 & 5.0

# 无线电发射设备

Radio Transmission Equipment

# 型号核准证

**Type Approval Certificate** 

劲达国际电子有限公司 (台湾):

根据《中华人民共和国无线电管理 In accordance with the provisions on the Radio

条例》,经审查,下列无线电发射设备 Regulations of the People's Republic of China, the following

符合中华人民共和国无线电管理规定和 radio transmission equipment, after examination, conforms

技术标准, 其核准代码为: CMLIT ID: 2019DJ6123 (M)

to the provisions with its CMHT ID:

有效期: 五年 Validity

(发证机关) Sealed by Issuing authority 2019年 7月 17日 Year Month Date

# 9.8. KC Certificate (South Korea)

#### BLE 4.2 & 5.0

	방송통신기자재등의 적합인증서
Certif	ficate of Broadcasting and Communication Equipments
삼호 또는 성명 Trade Name or Applicant	Raytac Corporation
기자재명칭(명칭) Equipment Name	특정소출력 무선기기(무선데이터통신시스템용 무선기기)
기본모델명 Basic Model Number	MDBT42Q
파생모델명 Series Model Number	MDBT42Q-P, MDBT42Q-U
인증번호 Certification No.	MSIP-CRM-ryt-MDBT42Q
제조자/제조국가 Manufacturer/ Country of Origin	Raytac Corporation / 대만
인증연월일 Date of Certification	2016-10-06
기타 Omers	

위 기자재는 「전파법」제58조의2 제2항에 따라 인증되었음을 증명합니다.

It is verified that foregoing equipment has been certificated under the Clause 2, Article 58-2 of Radio Waves Act.

2019년(Year) 05월(Month) 14일(Day)

국립전파연구원정



Director General of National Radio Research Agency

※ 인증 받은 방송통신기자재는 만드시 "적합성평가표시"를 부착하여 유통하여야 합니다. 위반시 파대료 처분 및 인증이 취소될 수 있습니다.

#### 9.9. RoHS & REACH Report

Please visit "Support" page of our website to download.

#### 9.10. End-Product Label

It is suggested using following content adding to package or user manual or label to obey the regulation. Any rules of end-product label shall refer to each certification for final reference.

### 9.10.1. FCC (USA)

The FCC statement should be included in the user manual when there is no enough space on label. Otherwise, it should be included on the label.

"This device complies with part 15 of the FCC rules. Operation is subject to the following two conditions. (1) This device may not cause harmful interference. (2) This device must accept any interference received, including interference that may cause undesired operation."

The final end product must be labeled in a visible area with the following: "Contain FCC ID: SH6MDBT42Q".

### 9.10.2. TELEC (Japan)

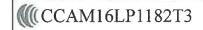
When manufacturer is placing the product on the Japanese market, the product must be affixed with the following Specified Radio Equipment marking:



#### 9.10.3. NCC (Taiwan)

請依下列標籤式樣自製標籤,標貼或印鑄於器材本體明顯處,始得販賣或公開陳列。

#### MDBT42Q-U Series



平台廠商必須於平台上標示字樣「本產品內含射頻模組:ID編號 CCAM16LP1182T3」。

「平台」定義如下:若器材組裝本案模組,消費者仍能正常使用該器材主要功能,該器材得視 為平台。若器材不組裝本案模組,消費者不能正常使用該器材主要功能,該器材不能視為平台。 該類不同廠牌型號器材組裝本案審驗模組後,須分別申請型式認證。

#### 9.10.4. IC (Canada)

The IC statement should be included in the user manual when there is no enough space on label. Otherwise, it should be included on the label.

"This device complies with Industry Canada license-exempt RSS Standard(s). Operation is subject to the following two conditions. (1) This device may not cause harmful interference. (2) This device must accept any interference received, including interference that may cause undesired operation.

Le présent appareil est conforme aux CNR d'Industrie Canada applicables aux appareils radio exempts de licence.L'exploitation est autorisée aux deux conditions suivantes: (1) l'appareil ne doit pas produire de brouillage, et (2) l'utilisateur de l'appareil doit accepter tout brouillage radioélectrique subi, même si le brouillage est susceptible d'en compromettre le fonctionnement."

The final end product must be labeled in a visible area with the following: "Contain IC ID: 8017A-MDBT42Q".

#### 10. Notes and Cautions

Module is not designed to last for a lifetime. Like general products, it is expected to be worn out after continuous usage through the years. To assure that product will perform better and last longer, please make sure you:

- Follow the guidelines of this document while designing circuit/end-product. Any
  discrepancy of core Bluetooth technology and technical specification of IC should refer to
  definition of Bluetooth Organization and Nordic Semiconductor as final reference.
- Do not supply voltage that is not within range of specification.
- Eliminate static electricity at any cost when working with the module as it may cause damage. It is highly recommended adding anti-ESD components to circuit design to prevent damage from real-life ESD events. Anti-ESD methods can be also applied in mechanical design.
- Do not expose modules under direct sunlight for long duration. Modules should be kept away from humid and salty air conditions, and any corrosive gasses or substances. Store it within -40°C to +125°C before and after installation.
- Avoid any physical shock, intense stress to the module or its surface.
- Do not wash the module. No-Clean Paste is used in production. Washing it will oxidize
  the metal shield and have chemistry reaction with No-Clean Paste. Functions of the
  module are not guaranteed if it has been washed.

The module is not suitable for life support device or system and not allowed to be used in destructive device or systems in any direct or indirect ways. The customer agrees to indemnify Raytac for any losses when applying modules in applications such as the ones described above.

# 11. Basic Facts for nRF52 Chip

Below chart shows basic spec for Nordic nRF52 family, which is helpful to understand the differences between each SoC. Any discrepancy shall refer to Nordic's technical document as final reference.

Nordic Solution	nRF52840	nRF52833	nRF52832	nRF52810	nRF52811
RAYTAC Model No.	MDBT50Q-1MV2 MDBT50Q-P1MV2 MDBT50Q-U1MV2	MDBT50Q-512K MDBT50Q-P512K MDBT50Q-U512K	MDBT42Q-512KV2 MDBT42Q-P512KV2 MDBT42 series MDBT42V series	MDBT42Q-192KV2 MDBT42Q-P192KV2	MDBT42Q-192KL MDBT42Q-P192KL
Bluetooth 5.1		V			V
Bluetooth 5 Long Range (125kbps)	v	v			v
Bluetooth 5 High Speed	V	v	V	V	V
Bluetooth 5 Ad. Extention (x8)	V	V	V	V	V
Flash (kBytes)	1024	512	512	192	192
RAM (kBytes)	256	128	64	24	24
ANT Plus	V	V	V	V	V
IEEE 802.15.4	V	V			V
ARM® TrustZone® Cryptocell	V				
USB	V	V			
QSPI	V				
NFC	V	V	v		
128	V	V	٧		
SPI, TWI, UART, PWM	V	V	٧	V	V
PDM	V	V	v	V	V
ADC, Comparators	V	V	٧	V	V
Supply Range (V)	1.7 to 5.5	1.7 to 5.5	1.7 to 3.6	1.7 to 3.6	1.7 to 3.6

# 12. Useful Links

- Nordic Infocenter: <a href="https://infocenter.nordicsemi.com/index.jsp">https://infocenter.nordicsemi.com/index.jsp</a>
   All the necessary technical files and software development kits of Nordic's chip are on this website.
- Nordic DevZone: <a href="https://devzone.nordicsemi.com/questions/">https://devzone.nordicsemi.com/questions/</a>
   A highly recommended website for firmware developer. Interact with other developers and Nordic's employees will help with your questions. The site also includes tutorials in detail to help you get started.

# Full List of Raytac's BLE Modules

# **Raytac Corporation Bluetooth Module Family**





#### MDBT40 & MDBT40-P Series

Nordic Solution	Raytac No.	IC Version	Antenna	RAM	Flash Memory
~DE51000	MDBT40-256V3		Chip	16 kb	256 K
IIKF31022	MDBT40-256RV3	3	Antenna	32 kb	256 K
	O 70	337	10. 10	30.	
~DE51000	MDBT40-P256V3	2	PCB	16 kb	256 K
IIKF31022	MDBT40-P256RV3	3	Antenna	32 kb	256 K
- 10	E AU 10 20			•	
nRF51422	MDBT40-ANT -256V3	- 3	Chip Antenna	16 kb	- 256 K
	MDBT40-ANT -256RV3			32 kb	230 K
	D D A.				
nRF51422	MDBT40-ANT -P256V3	3	PCB Antenna	16 kb	256 V
	MDBT40-ANT -P256 <mark>R</mark> V3			32 kb	256 K
SEP AND					
nRF51822	MDBT40-n256V3	3	N/A	16 kb	256 K
nRF51422	MDBT40-ANT -n256V3	3	N/A	16 kb	256 K
	Solution nRF51822 nRF51822 nRF51422 nRF51422	Solution         Raytac No.           nRF51822         MDBT40-256V3           MDBT40-256RV3           MDBT40-P256V3           MDBT40-P256RV3           MDBT40-ANT -256V3           MDBT40-ANT -256RV3           MDBT40-ANT -P256V3           MDBT40-ANT -P256V3           MDBT40-ANT -P256RV3           MDBT40-ANT -P256RV3	Solution         Raytac No.         Version           nRF51822         MDBT40-256V3         3           nRF51822         MDBT40-P256V3         3           nRF51822         MDBT40-P256RV3         3           nRF51422         MDBT40-ANT -256V3         3           nRF51422         MDBT40-ANT -P256V3         3           nRF51822         MDBT40-ANT -P256RV3         3           nRF51822         MDBT40-n256V3         3           nRF51422         MDBT40-ANT -P256V3         3	Solution         Raytac No.         Version         Antenna           nRF51822         MDBT40-256V3         3         Chip Antenna           nRF51822         MDBT40-P256RV3         3         PCB Antenna           nRF51422         MDBT40-P256RV3         3         Chip Antenna           nRF51422         MDBT40-ANT -256V3         3         Chip Antenna           nRF51422         MDBT40-ANT -P256V3         3         PCB Antenna           nRF51822         MDBT40-ANT -P256RV3         3         N/A	Solution   Raytac No.   Version   Antenna   RAM

# MDBT42Q Series (QFN Package IC)

Series	Nordic Solution	Raytac No.	IC Version	Antenna	RAM	Flash Memory	
MDBT42Q	nRF52832	MDBT42Q-512KV2	2		64 kb	512 K	
	nRF52810	MDBT42 <mark>Q</mark> -192K	1	Chip Antenna	24 kb	192 K	
	nRF52811	MDBT42Q-192KL	1				
MDBT42 <mark>Q</mark> -P	nRF52832	MDBT42Q-P512KV2	2	PCB Antonno	64 kb	512 K	
	nRF52810	MDBT42Q-P192K	1		0.4 Jah	192 K	
	nRF52811	MDBT42Q-P192KL	1.		24 kb		
400,000 10 10 10 10 10							
MDBT42Q-U	nRF52832	MDBT42Q-U512KV2	2	u.FL Connector	64 kb	512 K	

# MDBT42 Series (WLCSP Package IC)

Series	Nordic Solution	Raytac No.	IC Version	Antenna	RAM	Flash Memory
MDBT42	nRF52832	MDBT42-512KV2	2	Chip Antenna	- 64 kb	512 K
MDBT42-P		MDBT42-P512KV2		PCB Antenna		

Series	Nordic Solution	Raytac No.	IC Version	Antenna	RAM	Flash Memory
MDBT42V	nRF52832	MDBT42V-512KV2	2	Chip Antenna	64 kb	512 K
MDBT42V-P		MDBT42V-P512KV2		PCB Antenna		

# MDBT50Q Series (aQFN Package IC)

Series	Nordic Solution	Raytac No.	IC Version	Antenna	RAM	Flash Memory
MDBT50Q	nRF52840	MDBT50Q-1MV2	2	Chip Antenna	256 kb	1MB
	nRF52833	MDBT50Q-512K	1		128 kb	512 kb
MDBT50Q-P	nRF52840	MDBT50Q-P1MV2	2	PCB Antenna	256 kb	1MB
	nRF52833	MDBT50Q-P512K	1		128 kb	512 kb
MDBT50Q-U	nRF52840	MDBT50Q-U1MV2	2	u.FL	256 kb	1MB
	nRF52833	MDBT50Q-U512K	1	Connector	128 kb	512 kb

Dongle	nRF52840	MDBT50Q-RX	1,2	PCB Antenna	256 kb	1MB	
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### Release Note

- 2019/04/16 Version A: 1<sup>st</sup> release
- 2019/08/12 Version B:
  - (1) Added tolerance of PCB size in Chapter 2: Product Dimension.
  - (2) Updated label context in Chapter 4: Shipment Packaging Information.
  - (3) Updated certification information in Chapter 7: Antenna and Chapter 9: Certification.
  - (4) Refined description of default setting in Chapter 8: Reference Circuit.
  - (5) Updated Full List of Raytac's BLE Modules.
- 2019/12/19 Version C:
  - (1) Updated GPIO table in section 6 of Chapter 2: Product Dimension.
  - (2) Updated marking information on the shield in Chapter 4: Shipment Packaging Information.
  - (3) Updated Declaration ID information in section 1 of Chapter 9: Certification.
  - (4) Added nRF52833 to the chart in Chapter 11: Basic Facts of nRF52 Family & Full List of Raytac's BLE Modules.